IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	
Shunpei YAMAZAKI)	Examiner: Thanh T. Nguyen
Serial No. 10/772,586)	Art Unit: 2893
Filed: February 6, 2004)	Confirmation No.: 2329
For: METHOD OF MANUFACTURING		
SEMICONDUCTOR DEVICE AND DISPLAY		
DEVICE LITH IZING SOLUTION EJECTION		

INFORMATION DISCLOSURE STATEMENT

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. In accordance with the policies of the U.S. Patent and Trademark Office, only copies of newly cited foreign patents are being submitted.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initial a copy of this form be returned to the undersigned.

The commissioner is hereby authorized to charge any fee required in connection with this filing and any additional fees which may be required, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

By: /Jeffrey L. Costellia, Reg.# 35483/ Jeffrey L. Costellia

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